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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In Re Application of:

Donald K. Harper

Serial No.: 09/460,007

Group Art Unit: 2833

Filing Date: December 13, 1999

Examiner: Truc T. Nguyen

For: ELECTRICAL CONNECTOR HOUSING

DATE OF DEPOSIT: July 17, 2002

I HEREBY CERTIFY THAT THIS PAPER IS BEING DEPOSITED WITH THE UNITED STATES POSTAL SERVICE AS FIRST CLASS MAIL, POSTAGE PREPAID ON THE DATE INDICATED ABOVE AND IS ADDRESSED TO THE ASSISTANT COMMISSIONER FOR PATENTS, WASHINGTON, DC 20231

TYPED NAME: Jonathan M. Waldman
REGISTRATION NO.: 40,861

Assistant Commissioner for Patents
Washington DC 20231

TRANSMITTAL OF APPEAL BRIEF

1. Transmitted herewith in triplicate is the APPEAL BRIEF in this application with respect to the Notice of Appeal filed on June 20, 2002.

2. STATUS OF APPLICANT

- ☐ Applicant(s) has previously claimed small entity status under 37 C.F.R. §1.27 .
- ☐ Applicant(s) by its/their undersigned attorney, claims small entity status under 37 C.F.R. §1.27 as:
- ☐ an Independent Inventor
 - ☐ a Small Business Concern
 - ☐ a Nonprofit Organization.

3. **EXTENSION OF TERM**

The proceedings herein are for a patent application and the provisions of 37 CFR 1.136 apply.

- ☐ Applicant petitions for an extension of time under 37 CFR 1.136 (fees: 37 CFR 1.17(a)-(d)) for the total number of months checked below:

| | SMALL ENTITY | | NOT SMALL ENTITY | |
|--|--------------|-------|------------------|----------|
| <input type="checkbox"/> ONE MONTH EXTENSION OF TIME | \$55 | \$ | \$110 | \$ |
| <input type="checkbox"/> TWO MONTH EXTENSION OF TIME | \$200 | \$ | \$400 | \$ |
| <input type="checkbox"/> THREE MONTH EXTENSION OF TIME | \$460 | \$ | \$920 | \$ |
| <input type="checkbox"/> FOUR MONTH EXTENSION OF TIME | \$720 | \$ | \$1440 | \$ |
| <input type="checkbox"/> FIVE MONTH EXTENSION OF TIME | \$980 | \$ | \$1960 | \$ |
| <input type="checkbox"/> LESS ANY EXTENSION FEE ALREADY PAID | minus | (\$) | minus | (\$) |
| <input checked="" type="checkbox"/> APPEAL BRIEF | \$160 | \$ | \$320 | \$320.00 |
| TOTAL FEE DUE | | | | \$320.00 |

4. **FEE PAYMENT**

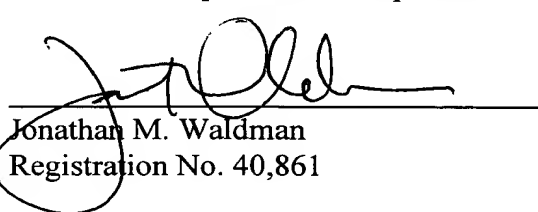
- ☒ A check in the amount of \$ 320.00 is attached. Please charge any deficiency or credit any overpayment to Deposit Account No. 23-3050.
- ☐ Please charge my Deposit Account No. 23-3050 in the amount of \$____. A duplicate of this transmittal is attached.

5. **FEE DEFICIENCY**

- ☒ If any additional extension and/or fee is required, this is a request therefor and to charge Deposit Account No. 23-3050.
- ☒ If any additional fee for claims is required, charge Deposit Account No. 23-3050.

6. ☒ The Commissioner is hereby requested to grant an extension of time for the appropriate length of time, should one be necessary, in connection with this filing or any future filing submitted to the U.S. Patent and Trademark Office in the above-identified application during the pendency of this application. The Commissioner is further authorized to charge any fees related to any such extension of time to deposit account 23-3050. This sheet is provided in duplicate.

Date: July 17, 2002



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For: ELECTRICAL CONNECTOR HOUSING

I, Jonathan M. Waldman, Registration No. 40,861 certify that this correspondence is being deposited with the U.S. Postal Service as First Class Mail in an envelope addressed to the Assistant Commissioner for Patents, Washington, D.C. 20231.

On July 17, 2002

Jonathan M. Waldman Reg. No. 40,861

Assistant Commissioner for Patents
Washington DC 20231

Sir:

APPELLANT'S BRIEF UNDER 37 C.F.R. § 1.192

This brief is in support of Appellant's appeal from the rejection of claims 22-31 dated May 22, 2002. A Notice of Appeal was filed on June 20, 2002.

A. REAL PARTY IN INTEREST

The inventor in the present application has assigned his interest to FCI AMERICAS TECHNOLOGY INC., f/k/a BERG TECHNOLOGY, INC. The original Assignment document, to BERG TECHNOLOGY, INC., was filed for recordation in the U.S. Patent and Trademark Office assignment system on January 14, 2000, and was recorded on January 14, 2000 at Reel 010556, Frame(s) 0342.

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7/31/02

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B. RELATED APPEALS AND INTERFERENCES

There are no related appeals or interferences.

C. STATUS OF CLAIMS

1. Claims 22-31 are pending. Claims 22-31 are reproduced in Appendix A hereto. All claims (22-31) stand rejected under 35 U.S.C. § 103(a), and are the subject of the present appeal.

2. Claims 22-28 stand rejected under 35 U.S.C. § 103(a) as being unpatentable over Applicant's Admitted Prior Art in view of U.S. Patent No. 5,788,510 to Walker.

3. Claims 29-31 stand rejected under 35 U.S.C. § 103(a) as being unpatentable over Applicant's Admitted Prior Art in view of Walker and further in view of U.S. Patent No. 6,033,236 to McHugh et al.

D. STATUS OF AMENDMENTS

No Amendments have been filed subsequent to the rejection.

E. SUMMARY OF THE INVENTION

As described in the "Background of the Invention", and will be slightly elaborated upon here, in the mounting of an integrated circuit (IC) on a plastic or ceramic substrate, the use of a ball grid array (BGA) has become common and well established technology. In a BGA package, spherical solder balls attached to the IC package are positioned on electrical contact pads of a circuit substrate to which a layer of solder paste has been applied. The unit is then heated to a temperature at which the solder paste and at least a portion of each solder ball melts and fuses to an underlying conductive pad formed on the circuit substrate. The IC is thereby connected to the substrate without need of external leads on the IC.

More complex, however, is, a corresponding process and structure for mounting an electrical connector or similar component on a printed wiring board (PWB) or other substrate. It is important that the substrate-engaging surfaces of the solder balls are co-planar to form a substantially flat mounting interface, so that in the final application the balls will reflow and solder evenly to a planar

printed circuit board substrate. Any significant differences in solder co-planarity on a given substrate can cause poor soldering performance when the connector is reflowed onto a printed circuit board.

A problem presented in soldering connectors to a substrate is that connectors often have insulative housings that have relatively complex shapes. Residual stresses in such housings can result from the molding process, from the buildup of stress as a result of contact insertion, or a combination of both. These housings may become warped or twisted either initially from the molding process or upon heating to temperatures necessary for subsequent processing, such as temperatures necessary to reflow the solder balls. Such warping or twisting of the connector housing can cause a mismatch between the connector assembly and the PWB, resulting in non-co-planarity and unreliable soldering because the surface mounting elements, such as solder balls, are not sufficiently in contact with the solder paste or close enough to the PWB prior to soldering across the entire mounting interface of the connector.

The present invention is directed to a connector that exhibits high co-planarity along the mounting interface. Co-planarity of the surface mounting interface of the connector is maintained by providing an insulative connector housing in which stress buildup is avoided. The present invention accommodates the deformation or warpage caused by thermal cycling that would otherwise cause the stress buildup during a reflow process employing heat. The connector avoids stress buildup by providing a connector housing that incorporates compliant sections corresponding to the areas where the greatest deformation in the connector is expected. According to this aspect of the invention, the housing has notches or slots at locations furthest from the neutral point of the connector. By means of this arrangement, stress buildup is avoided during a reflow process employing heat, so as to minimize warping and twisting of the housing.

Fig. 3 is a side view of an exemplary BGA interface connector in accordance with the present invention, Fig. 4 is a perspective view of the BGA interface connector of Fig. 3, and Fig. 5 is a bottom perspective view of the BGA interface connector of Fig. 3. The housing 15 has openings 12 in sidewalls that are placed at desired locations so as to allow the housing 15 to be compliant along desired axes. As a result, a reduction in solder joint stress between the connector 10 and its mounting substrate (such as an underlying PWB) results. Thus, during thermal cycling, the effects of

the differential in the coefficient of thermal expansion of the PWB substrate and the connector 10 are minimized. Moreover, the co-planarity of the contacts is improved, and stress buildup is avoided during a reflow process employing heat.

F. ISSUES

1. Whether claims 22-28 patentably define over Applicant's Admitted Prior Art in view of U.S. Patent No. 5,788,510 to Walker.

2. Whether claims 29-31 patentably define over Applicant's Admitted Prior Art in view of Walker and further in view of U.S. Patent No. 6,033,236 to McHugh et al.

G. GROUPING OF CLAIMS

Claims 22-31 stand or fall together.

H. ARGUMENT

Claims 22-28 have been rejected under 35 U.S.C. § 103(a) as being unpatentable over Applicant's Admitted Prior Art ("APA") in view of U.S. Patent No. 5,788,510 to Walker. Claims 29-31 have been rejected under 35 U.S.C. § 103(a) as being unpatentable over Applicant's Admitted Prior Art in view of Walker and further in view of U.S. Patent No. 6,033,236 to McHugh et al.

Concerning independent claims 22 and 28, in the rejection, the Examiner states that:

Walker¹ discloses a connector comprising a notch (E1) located only at a location furthest from neutral point of the housing...

Although Walker does not specifically disclose the slot (E1) is for the purpose of preventing warpage problem of the housing caused by thermal cycling. The structure disclosed by Walker would inherently

¹ The APA requires little comment here since it admittedly lacks the features of the invention, hence the Examiner's need to apply or add Walker and/or McHugh.

obtain the advantage of the slot being present in the housing. Thus, it is capable to preventing warpage in the housing.

It would have been obvious to one having ordinary skill in the art to modify the connection housing of Applicant's APA with a slot only at furthest location from neutral point of the housing. It has been held that a recitation with respect to the manner in which a claimed apparatus is intended to be employed does not differentiate the claimed apparatus from a prior art apparatus satisfying the claimed structural limitations. *Ex parte Marsham*, 2 USPQ2d 1647 (1987).

Rejection of May 22, 2002, at pages 2-3.

This rejection should be reversed for various reasons.

1. Walker does not actually “teach anything” concerning “notches”

Regarding Walker, the Examiner places great credence on what he labels “E1” in Figure 1 of Walker. The Examiner states that E1 of Walker is furthest from Walker's neutral point, would inherently obtain the advantage of preventing warpage because it is capable of doing so, and therefore alleges that claims 22-28 are obvious over the APA in view of Walker.

However, the reason the Examiner has to label the “notch” of Walker “E1” is because Walker himself does not label it anything -- seemingly reducing this feature to some kind of insignificant triviality. Indeed, in actuality, Walker does not --to use the Examiner's words-- “specifically disclose” *anything* about the “notch” and is absolutely silent to why that “notch” is there and what purpose it serves. Walker's “notch” is certainly not there to prevent warpage during a reflow process as required by the present claims because as a through-hole connector (see also, ¶2 below), Walker's connector would not go through the heat history and heat profile associated with a reflow process.

To think that one skilled in the art would look at Walker and actually incorporate such a blind “teaching” concerning these “notches” is just not reasonable.

2. Walker is in no way related to BGA technology or its problems

Walker is unrelated to a connector such as the APA that is involved in a reflow process employing heat. Walker is a completely different type of connector faced with completely different issues. Walker is a through-hole connector as indicated by solder tails 76. Co-planarity is not critical in such a connector because of the larger electrical contact surfaces of solder tails 76. Accordingly, even if, somehow through some type of heating, Walker's housing was warped, etc., the warpage's effect on electrical performance might completely go unnoticed due to the fact that the long solder tails 76 can take up the slack of dimensional imperfections. Thus, no one skilled in the art would know that Walker's notches cure a warpage problem because there would be no electrical evidence of a warpage problem in the first place.

Moreover, the combination of the APA and Walker is simply not a valid combination. The APA is a "board-to-board" type of connector employing surface mount connecting techniques, and Walker is a through-hole connector. Such connectors cannot be combined.

One skilled in the art, faced with complex co-planarity problems and CTE mis-match problems in a high-speed board-to-board surface mount connector such as disclosed in the APA would never look to a low-tech (relative to the APA) through-hole connector such as Walker that does not face any of the same problems. Thus, for at least this reason, the combination of the two must fail.

3. The Examiner's Inherency Argument is Flawed

The Examiner states that Walker's cutout would inherently obtain the advantages of Applicant's. This is a flawed statement.

a. No General Factual Support For Such Conclusion

The Examiner states Walker's "notch" is inherently capable of preventing warpage. There is absolutely nothing in Walker to support that conclusion. Indeed, "notch" E1 can be a manifestation of the manufacturing of the part, i.e., positioning tool placement. Furthermore, there is no reason not to believe all the "notches" must be present together. The absolute silence of Walker places too many doubts as to the "teaching".

On the other hand, Applicant claims a specific location for their area of reduced rigidity. In claim 22, for example, the areas of reduced rigidity are located where it is known that stress will

build up due to the heat of the reflow process and the materials from which the connector housing are made. Such locations can be easily predicted by modeling as well as testing and are then predetermined.

b. No Factual Support For Such Conclusion That Meets *MPEP* Requirements

This also fails on sheer lack of evidence. The Examiner's conclusory statement does not rise to the almost sheer certainty required by *MPEP* §2112 ("may" is not sufficient; "characteristic necessarily flows"). Furthermore, there is certainly no objective evidence or cogent technical reasoning to support the conclusion of inherency. *Id.*

4. Reliance on *Ex Parte Marsham*

Though the final paragraph of the Examiner's rejection starts off with "It would have been obvious to...", the Examiner does not actually provide the necessary motivation to support a 35 USC §103(a) obviousness rejection. Rather, it appears that the Examiner is relying on the *Marsham* case for his motivation. Admittedly, reliance upon case law for motivation is sometimes proper.

However, even if *Marsham* were applicable and that somehow boot-strapped Walker's "notches" to the level of significance of Applicant's specifically located areas of reduced rigidity, that would not detract from the fact that the Examiner's rejection still lacks the motivation to provide the APA with the Walker "notch". In other words, even if the Board concludes that despite Walker's silence, his "notch" does either do everything Applicant's areas of reduced rigidity does or can do everything or can be used to do everything Applicant's areas of reduced rigidity does, the logical step still has to be made that it would have been obvious to provide the APA with Walker's "notch" when Walker provides no motivation or even implicit suggestion as to why one skilled in the art would want to do so.

Next, the Examiner's reliance on *Marsham* for his motivation is misplaced because *Marsham* is a §102 anticipation case and not a §103 obviousness case. The case speaks nothing on motivation to combine. The case discusses whether one device that is exactly the same as another except for intended use can anticipate the other. In the instant application there are not two identical devices. Therefore, the applicability of *Marsham* is misplaced.

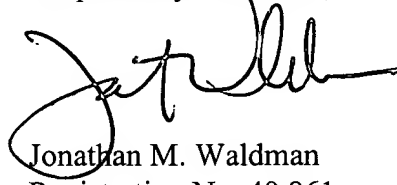
5. McHugh et al. fails to cure the deficiencies of the APA and Walker

The Examiner states that McHugh et al. discloses a portion of a housing is removed to form a passageway, thereby defining an open slot. The Examiner states that the open slot will prevent a warping problem and points to a passage in McHugh et al. that describes warpage of the housing (col. 3, lines 13-22). The Examiner concludes that it would have been obvious to one skilled in the art to remove a portion of the housing where the stress is high as taught by McHugh et al. However, McHugh et al. is unrelated to a reflow process employing heat, and therefore, the slot is not there to prevent warpage during a reflow process. Instead, the slot in McHugh et al. is merely related to a tolerance space for any warpage that may occur due to the relatively large height of the housing. Furthermore, McHugh's slot is related to the proper retention of the vertically positioned contact within the housing and not the maintenance of co-planarity of a surface mount connection between a connector and its associated substrate. Thus, one skilled in the art, when seeking to prevent warpage during a reflow process, would not look to the slot of McHugh et al.

I. CONCLUSION

For the foregoing reasons, Appellant submits that the inventions recited in claims 22-31 fully comply with the requirements of 35 U.S.C. § 103(a). Appellant therefore requests that this patent application be remanded to the Examiner with an instruction to both withdraw the rejections for alleged unpatentability and allow the appealed claims.

Respectfully submitted,

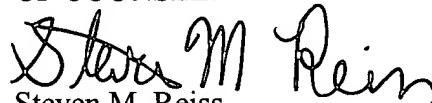


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APPENDIX A***Claims on Appeal***

22. An electrical connector, comprising:

a housing including a peripheral wall surrounding and rising above an array of contact receiving passageways and having a perimeter edge;

a plurality of separate surface mount contacts for connecting said housing to a substrate by a reflow process employing heat; and

areas of reduced rigidity in portions of said peripheral wall of the housing at which stress builds up due to the heat of the reflow process, each of the areas of reduced rigidity comprising portions of removed housing extending substantially perpendicular to a surface of the housing and extending through a distal end of said peripheral wall of the housing from an inner face to an outer face of the peripheral wall;

such that said areas of reduced rigidity contribute to said plurality of separate surface mount contacts better retaining their co-planarity during and after the reflow process.

23. The electrical connector according to claim 22, wherein the areas of reduced rigidity in the housing are located at positions furthest from a neutral point of the connector.

24. The electrical connector according to claim 22, wherein each of the areas of reduced rigidity comprises one of a notch and a slot.

25. The electrical connector according to claim 22, wherein the areas of reduced rigidity are disposed to absorb stress and accommodate warp.

26. The electrical connector according to claim 22, wherein the housing is made from a dielectric material.
27. The electrical connector according to claim 22, wherein the plurality of separate surface mount contacts comprise solder balls.
28. A method of reducing rigidity in a housing of an electrical connector having a plurality of separate surface mount contacts, comprising:
- determining a location on said housing which may build up stress during a reflow process employing heat in which the plurality of separate surface mount contacts connect said housing to a substrate; and
 - removing a portion of the housing extending substantially perpendicular to a surface of the housing at said location.
29. The method according to claim 28, wherein said location is generally furthest from a neutral point of the connector.
30. The method according to claim 28, wherein said location is located to absorb stress and accommodate warp.
31. The method according to claim 28, wherein the portion is one of a slot and a notch.